



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

*WFC*

U.S. Patent No. 6,794,729 B2

Issued: September 21, 2004

Toru MORI, et al.

Serial No.: 10/085,116

Group Art Unit: 2811

Filing Date: March 1, 2002

Examiner: Nguyen, Cuong Quang

For: STACKED CAPACITOR AND METHOD OF FORMING THE SAME AS WELL  
AS SEMICONDUCTOR DEVICE USING THE SAME AND CIRCUIT BOARD  
USING THE SAME

ATTN: Certificate of Corrections Branch

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Certificate

OCT 05 2004

of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Sir:

The undersigned respectfully requests that a Certificate of Correction be issued for the above-identified patent. In particular, it is requested that the Inventor names be corrected to:

**Toru Mori, Tokyo (JP); Takao ~~Yamakazi~~ Yamazaki, Tokyo (JP); Koichiro Nakase, Tokyo (JP)**, as reflected on the Declaration. Since the error was made by the Patent and Trademark Office, no fee is submitted herewith.

Please forward a Certificate of Correction to the address shown below. Any telephone inquiries related to this matter should be directed to the undersigned at the number shown below.

The Commissioner is hereby authorized to charge any deficiency in fees or credit any overpayment in fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

*Frederick E. Cooperrider*

Frederick E. Cooperrider  
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Date: 9/30/04

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6 OCT 2004

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO : 6,794,729 B2

DATED : September 21, 2004

INVENTOR(S) : Toru Mori, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the cover page, column 1, please change the [75] Inventors to:

-- Toru Mori, Tokyo (JP); Takao Yamazaki, Tokyo (JP); Koichiro Nakase, Tokyo (JP) --

MAILING ADDRESS OF SENDER: Frederick E. Cooperrider  
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PATENT NO. 6,794,729 B2

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